EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	11/120588	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:35
L2	0	l1 and error	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:35
L3	1	l1 and (cost or perform$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:45
L4	0	l1 and (knowledge adj rule)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:45
L5	0	l1 and (knowlege adj rule)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:45
L6	0	l1 and (knowledge)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:45
L7	0	l1 and (rule)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 15:45
L10	1	"6853952".pn. and a$1cyclic$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 16:05
S1	10	AKKARAM-S AKKARAM-SRIKANTH GAO-DONGMING GAO-DONG-MING ZHOU-YOUDONG	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:11

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S2	0	10/065740	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/27 17:03
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S4	993	S3 same (scenario or sequence)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:14
S5	416	S4 and (graph or chart)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:15
S6	32	S4 and ((graph or chart) same edge)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:19
S7	4	S6 and a$1cyclic$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 16:05
S8	372	S3 and a$1cyclic$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:24
S9	7	S3 and a$1cyclic$2.ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:24
S10	6	S9 not S7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:24
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S12	6	(material same (process$5 or manufactur$5 or develop$5) same (model$5 or simulat$5 or emulat$5)) same dag	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:34
S13	1669	703/2.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:34
S14	196	703/9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/28 10:34

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Kambhampati, S.; Cutkosky, M.R.; Tenenbaum, J.M.; Lee, S.H.;
Systems, Man and Cybernetics, IEEE Transactions on
Volume 23, Issue 6, Nov.-Dec. 1993 Page(s):1503 - 1518
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Software Engineering, IEEE Transactions on
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Engineering Management, IEEE Transactions on
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Liangzhao Zeng; Benatallah, B.; Ngu, A.H.H.; Dumas, M.; Kalagnanam, J.; Chang, H.;
Software Engineering, IEEE Transactions on
Volume 30, Issue 5, May 2004 Page(s):311 - 327
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5. **Artificial intelligence and grids: workflow planning and beyond**
Gil, Y.; Deelman, E.; Blythe, J.; Kesselman, C.; Tangmunarunkit, H.;
Intelligent Systems, IEEE [see also IEEE Intelligent Systems and Their Applications]
Volume 19, Issue 1, Jan-Feb 2004 Page(s):26 - 33
Digital Object Identifier 10.1109/MIS.2004.1265882

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Chew, S.F.; Lawley, M.A.; Reveliotis, S.A.;
Decision and Control, 2003. Proceedings. 42nd IEEE Conference on
Volume 4, 9-12 Dec. 2003 Page(s):3735 - 3741 vol.4
Digital Object Identifier 10.1109/CDC.2003.1271730
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7. **Graphical animation of behavior models**
Magee, J.; Pryce, N.; Giannakopoulon, D.; Kramer, J.;
Software Engineering, 2000. Proceedings of the 2000 International Conference on
4-11 June 2000 Page(s):499 - 508
Digital Object Identifier 10.1109/ICSE.2000.870440
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8. **The CORD approach to extensible concurrency control**
Heineman, G.T.; Kaiser, G.E.;
Data Engineering, 1997. Proceedings. 13th International Conference on
7-11 April 1997 Page(s):562 - 571
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1 Use of directed acyclic graph analysis in generating instructions for multiple users
Margaret Mitchell
December 2001 **Australian symposium on Information visualisation - Volume 9 CRPITS '01**
Publisher: Australian Computer Society, Inc.
Full text available: pdf(684.99 KB) Additional Information: full citation, abstract, references, index terms

Directed acyclic graphs are a common method of representing groups of tasks that have an underlying partial order. Graphs have the advantage of easily representing nonlinear relationships, but relationships between groups of tasks may go unnoticed as a group of tasks becomes large. Textual instructions have the advantage of allowing explicit reference to tasks or groups of tasks, but they do not facilitate users working in parallel. We present the use of directed acyclic graph analysis as a mean ...

2 Budgeting, simulation and statistical timing: Optimal integer delay budgeting on directed acyclic graphs
E. Bozorgzadeh, S. Ghiasi, A. Takahashi, M. Sarrafzadeh
June 2003 **Proceedings of the 40th conference on Design automation**
Publisher: ACM Press
Full text available: pdf(373.56 KB) Additional Information: full citation, abstract, references, citings, index terms

Delay budget is an excess delay each component of a design can tolerate under a given timing constraint. Delay budgeting has been widely exploited to improve the design quality. We present an optimal integer delay budgeting algorithm. Due to numerical instability and discreteness of libraries of components during library mapping in design optimization flow, integer solution for delay budgeting is essential. We prove that integer budgeting problem - a 20-year old open problem in design optimizati ...

3 Transitive closure algorithms based on graph traversal
Yannis Ioannidis, Raghu Ramakrishnan, Linda Winger
September 1993 **ACM Transactions on Database Systems (TODS)**, Volume 18 Issue 3
Publisher: ACM Press
Full text available: pdf(4.34 MB) Additional Information: full citation, abstract, references, citings, index terms

Several graph-based algorithms have been proposed in the literature to compute the transitive closure of a directed graph. We develop two new algorithms (Basic_TC and

Gobal_DFTC) and compare the performance of their implementations in a disk-based environment with a well-known graph-based algorithm proposed by Schmitz. Our algorithms use depth-first search to traverse a graph and a technique called marking to avoid processing some of the arcs in the graph. They compute the ...

Keywords: depth-first search, node reachability, path computations, transitive closure

4 A compression technique to materialize transitive closure

H. V. Jagadish

December 1990 **ACM Transactions on Database Systems (TODS)**, Volume 15 Issue 4

Publisher: ACM Press

Full text available: pdf(3.25 MB) Additional Information: full citation, abstract, references, citings, index terms, review

An important feature of database support for expert systems is the ability of the database to answer queries regarding the existence of a path from one node to another in the directed graph underlying some database relation. Given just the database relation, answering such a query is time-consuming, but given the transitive closure of the database relation a table look-up suffices. We present an indexing scheme that permits the storage of the pre-computed transitive closure of a database re ...

5 On loops, dominators, and dominance frontiers

September 2002 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 24 Issue 5

Publisher: ACM Press

Full text available: pdf(351.81 KB) Additional Information: full citation, abstract, references, index terms, review

This article explores the concept of loops and loop nesting forests of control-flow graphs, using the problem of constructing the dominator tree of a graph and the problem of computing the iterated dominance frontier of a set of vertices in a graph as guiding applications. The contributions of this article include: (1) An axiomatic characterization, as well as a constructive characterization, of a family of loop nesting forests that includes various specific loop nesting forests that have been p ...

Keywords: Dominator, graph transformation, irreducible graph, iterated dominance frontier, least common ancestor, loop, loop nesting forest, sparse evaluation

6 On loops, dominators, and dominance frontier

G. Ramalingam

May 2000 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2000 conference on Programming language design and implementation PLDI '00**, Volume 35 Issue 5

Publisher: ACM Press

Full text available: pdf(207.48 KB) Additional Information: full citation, references, citings, index terms

7 A new viewpoint on code generation for directed acyclic graphs

S. Liao, K. Keutzer, S. Tjiang, S. Devadas

January 1998 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 3 Issue 1

Publisher: ACM Press

Full text available: pdf(661.59 KB) Additional Information: full citation, abstract, references, index terms

We present a new viewpoint on code generation for directed acyclic graphs (DAGs). Our formulation is based on binate covering, the problem of satisfying, with minimum cost, a set of disjunctive clauses, and can take into account commutativity of operators and of the machine model. An important contribution of this work is a set of necessary and sufficient conditions for a valid schedule to be derived, based on the notion of worms and worm-partitions ...

Keywords: binate covering, code generation, directed acyclic graphs

8 A new guaranteed heuristic for the software pipelining problem

Pierre-Yves Calland, Alain Darte, Yves Robert

January 1996 **Proceedings of the 10th international conference on Supercomputing**

Publisher: ACM Press

Full text available: pdf(892.93 KB) Additional Information: full citation, references, index terms

Keywords: circuit retiming, cyclic scheduling, guaranteed heuristic, list scheduling, software pipelining

9 Hierarchical directed acyclic graph kernel: methods for structured natural language data

Jun Suzuki, Tsutomu Hirao, Yutaka Sasaki, Eisaku Maeda

July 2003 **Proceedings of the 41st Annual Meeting on Association for Computational Linguistics - Volume 1 ACL '03**

Publisher: Association for Computational Linguistics

Full text available: pdf(180.39 KB) Additional Information: full citation, abstract, references

This paper proposes the *"Hierarchical Directed Acyclic Graph (HDAG) Kernel"* for structured natural language data. The HDAG Kernel directly accepts several levels of both chunks and their relations, and then efficiently computes the weighed sum of the number of common attribute sequences of the HDAGs. We applied the proposed method to question classification and sentence alignment tasks to evaluate its performance as a similarity measure and a kernel function. The results of the experiment ...

10 Use of graph-theoretic models for optimal relational database accesses to perform join

Sakti Pramanik, David Ittner

March 1985 **ACM Transactions on Database Systems (TODS)**, Volume 10 Issue 1

Publisher: ACM Press

Full text available: pdf(1.11 MB) Additional Information: full citation, abstract, references, citings, index terms, review

A graph model is presented to analyze the performance of a relational join. The amount of page reaccesses, the page access sequence, and the amount of buffer needed are represented in terms of graph parameters. By using the graph model formed from the index on the join attributes, we determine the relationships between these parameters. Two types of buffer allocation strategies are studied, and the upper bound on the buffer size with no page reaccess is given. This bound is shown to be the ...

11 Semantics-preserving procedure extraction

Raghavan Komondoor, Susan Horwitz

January 2000 **Proceedings of the 27th ACM SIGPLAN-SIGACT symposium on Principles of programming languages**

Publisher: ACM Press
Full text available: pdf(2.18 MB) Additional Information: full citation, abstract, references, index terms

Procedure extraction is an important program transformation that can be used to make programs easier to understand and maintain, to facilitate code reuse, and to convert "monolithic" code to modular or object-oriented code. Procedure extraction involves the following steps: The statements to be extracted are identified (by the programmer or by a programming tool). If the statements are not contiguous, they are moved together so that they ...

12 A correctness proof for combinator reduction with cycles

William M. Farmer, John D. Ramsdell, Ronald J. Watro
January 1990 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 12 Issue 1
Publisher: ACM Press
Full text available: pdf(875.60 KB) Additional Information: full citation, abstract, references, citings, index terms, review

Turner popularized a technique of Wadsworth in which a cyclic graph rewriting rule is used to implement reduction of the fixed point combinator Y. We examine the theoretical foundation of this approach. Previous work has concentrated on proving that graph methods are, in a certain sense, sound and complete implementations of term methods. This work is inapplicable to the cyclic Y rule, which is unsound in this sense since graph normal forms can exist withou ...

13 Session 9B: Edge disjoint paths revisited

Chandra Chekuri, Sanjeev Khanna
January 2003 **Proceedings of the fourteenth annual ACM-SIAM symposium on Discrete algorithms**
Publisher: Society for Industrial and Applied Mathematics
Full text available: pdf(1.12 MB) Additional Information: full citation, abstract, references, citings, index terms

The approximability of the maximum edge disjoint paths problem (EDP) in directed graphs was seemingly settled by the $\Omega(m^{1/2-\epsilon})$-hardness result of Guruswami et al. [10] and the $O(\sqrt{m})$ approximation achievable via both the natural LP relaxation [19] and the greedy algorithm [11, 12]. Here m is the number of edges in the graph. However, we observe that the hardness of approximation shown in [10] applies to *sparse* graphs and hence when ...

14 Routing 1: On delivery times in packet networks under adversarial traffic

Adi Rosén, Michael S. Tsirkin
June 2004 **Proceedings of the sixteenth annual ACM symposium on Parallelism in algorithms and architectures**
Publisher: ACM Press
Full text available: pdf(241.99 KB) Additional Information: full citation, abstract, references, index terms

We consider packet networks and make use of the "adversarial queuing theory" model [10]. We are interested in the question of guaranteeing that all packets are actually delivered to destination, and of having an upper bound on the delivery times of all packets. Whether this is possible against all rate-1 adversaries was previously posed as an open question [15, 10].Among other, we give a queuing policy that guarantees bounded delivery time whenever the rate-1 adversary itself can deliver all pac ...

Keywords: adversarial queuing theory, delivery time, packet networks, protocols, stability

15 Multimedia: Handover cost and mobility-safety of content streams

Sasu Tarkoma, Jaakko Kangasharju
October 2005 **Proceedings of the 8th ACM international symposium on Modeling, analysis and simulation of wireless and mobile systems MSWiM '05**
Publisher: ACM Press
Full text available: pdf(172.32 KB) Additional Information: full citation, abstract, references, index terms

In this paper we investigate the handover cost and mobility-safety of content streams. Content streams are continuous flows of information from one node in a distributed network to another. The flows are established using publish/subscribe primitives and content-based routing of information. We examine two useful properties for mobility-aware content routing systems, namely completeness and mobility-safety. Then we determine the topology update cost for three interesting topologies, a number of ...

Keywords: event systems, mobility support, publish/subscribe

16 Research track poster: Unweaving a web of documents

R. Guha, Ravi Kumar, D. Sivakumar, Ravi Sundaram
August 2005 **Proceeding of the eleventh ACM SIGKDD international conference on Knowledge discovery in data mining KDD '05**
Publisher: ACM Press
Full text available: pdf(643.90 KB) Additional Information: full citation, abstract, references, index terms

We develop an algorithmic framework to decompose a collection of time-stamped text documents into semantically coherent threads. Our formulation leads to a graph decomposition problem on directed acyclic graphs, for which we obtain three algorithms --- an exact algorithm that is based on minimum cost flow and two more efficient algorithms based on maximum matching and dynamic programming that solve specific versions of the graph decomposition problem. Applications of our algorithms include super ...

Keywords: graph algorithms, graph decomposition, news threads

17 A survey of graph layout problems

Josep Díaz, Jordi Petit, Maria Serna
September 2002 **ACM Computing Surveys (CSUR)**, Volume 34 Issue 3
Publisher: ACM Press
Full text available: pdf(1.47 MB) Additional Information: full citation, abstract, references, citings, index terms

Graph layout problems are a particular class of combinatorial optimization problems whose goal is to find a linear layout of an input graph in such way that a certain objective cost is optimized. This survey considers their motivation, complexity, approximation properties, upper and lower bounds, heuristics and probabilistic analysis on random graphs. The result is a complete view of the current state of the art with respect to layout problems from an algorithmic point of view.

Keywords: Approximation algorithms, complexity, embedding, heuristics, layout, parameterized complexity, random graphs

18 A model for electronic documents

Richard Koo
January 1989 **ACM SIGOIS Bulletin**, Volume 10 Issue 1
Publisher: ACM Press
Full text available: Additional Information:

pdf(445.48 KB) full citation, abstract, citings, index terms

A model for electronic documents is proposed. This model has two components: a directed acyclic graph structure and a set of constraints. These two components are used to describe the relationships among the data of documents. This model is more general than models previously proposed.

19 Models of machines and computation for mapping in multicomputers

Michael G. Norman, Peter Thanisch
September 1993 **ACM Computing Surveys (CSUR)**, Volume 25 Issue 3
Publisher: ACM Press
Full text available: pdf(3.49 MB) Additional Information: full citation, references, citings, index terms

Keywords: mapping, multicomputer load balancing, multicomputers, partitioning, scheduling

20 Finding least common ancestors in directed acyclic graphs

Michael A. Bender, Giridhar Pemmasani, Steven Skiena, Pavel Sumazin
January 2001 **Proceedings of the twelfth annual ACM-SIAM symposium on Discrete algorithms**
Publisher: Society for Industrial and Applied Mathematics
Full text available: pdf(777.07 KB) Additional Information: full citation, references, index terms

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1 Types and persistence in database programming languages
Malcolm P. Atkinson, O. Peter Buneman
June 1987 **ACM Computing Surveys (CSUR)**, Volume 19 Issue 2
Publisher: ACM Press
Full text available: pdf(7.91 MB) Additional Information: full citation, abstract, references, citings, index terms, review

Traditionally, the interface between a programming language and a database has either been through a set of relatively low-level subroutine calls, or it has required some form of embedding of one language in another. Recently, the necessity of integrating database and programming language techniques has received some long-overdue recognition. In response, a number of attempts have been made to construct programming languages with completely integrated database management systems. These lang ...

2 Testability of 2-level AND/EXOR circuits
R. Drechsler, H. Hengster, H. Schafer, J. Hartmann, B. Becker
March 1997 **Proceedings of the 1997 European conference on Design and Test**
Publisher: IEEE Computer Society
Full text available: pdf(644.20 KB) Publisher Site Additional Information: full citation, abstract, citings

It is often stated that AND/EXOR circuits are much easier to test than AND/OR circuits. This statement only holds for restricted classes of AND/EXOR expressions, like positive polarity Reed-Muller expressions and fixed polarity Reed-Muller expressions. For these two classes of circuits good deterministic testability properties are known. In this paper we show that for these circuits also good random pattern testability can be proven. An input probability distribution is given which yields a shor ...

Keywords: Boolean functions, biased random patterns, deterministic testability properties, expected test length, fixed polarity Reed-Muller expressions, input probability distribution, positive polarity Reed-Muller expressions, random pattern testability, restricted classes, testability, two-level AND/EXOR circuits

3 Circuit-level optimizations: Probabilistic dual-Vth leakage optimization under variability
Azadeh Davoodi, Ankur Srivastava
August 2005 **Proceedings of the 2005 international symposium on Low power**

electronics and design ISLPED '05

Publisher: ACM Press

Full text available: pdf(181.86 KB) Additional Information: full citation, abstract, references, index terms

In this paper we address the problem of growing leakage variability through effective dual-threshold voltage assignment. We propose a probabilistic dynamic programming-based method to assign dual-threshold voltages such that the overall expected leakage is minimized under a given probability of violating the timing constraint (timing yield). The key characteristics of our strategy are two pruning criteria that stochastically identify pareto-optimal solutions and prune the sub-optim ...

Keywords: automatic synthesis, leakage, optimization, process variations

4 Path hashing to accelerate delay fault simulation

Manfred Henftling, Hannes C. Wittmann, Kurt J. Antreich

June 1994 **Proceedings of the 31st annual conference on Design automation**

Publisher: ACM Press

Full text available: pdf(154.67 KB) Additional Information: full citation, references, citings, index terms

5 On Statistical Timing Analysis with Inter- and Intra-Die Variations

Hratch Mangassarian, Mohab Anis

March 2005 **Proceedings of the conference on Design, Automation and Test in Europe - Volume 1**

Publisher: IEEE Computer Society

Full text available: pdf(463.21 KB) Additional Information: full citation, abstract

In this paper, we highlight a fast, effective and practical statistical approach that deals with inter and intra-die variations in VLSI chips. Our methodology is applied to a number of random variables while accounting for spatial correlations. Our methodology sorts the Probability Density Functions (PDFs) of the critical paths of a circuit based on a confidence-point. We show the mathematical accuracy of our method as well as implement a typical program to test it on various benchmarks. We find ...

6 Manufacturing applications: A virtual environment for simulating manufacturing operations in 3D

Ravi Chawla, Amarnath Banerjee

December 2001 **Proceedings of the 33nd conference on Winter simulation**

Publisher: IEEE Computer Society

Full text available: pdf(337.90 KB) Additional Information: full citation, abstract, references, index terms

This paper presents a method for simulating basic manufacturing operations (unload, load, process, move, and store) in a 3D virtual environment. The virtual environment provides a framework for representing a facility layout in 3D, which encapsulates the static and the dynamic behavior of the manufacturing system. The 3D manufacturing objects in the facility are mapped with the nodes in the framework. The framework, a modified scenegraph structure, is a tree structure, which can be manipulated b ...

7 ISEGEN: Generation of High-Quality Instruction Set Extensions by Iterative Improvement

Partha Biswas, Sudarshan Banerjee, Nikil Dutt, Laura Pozzi, Paolo Ienne

March 2005 **Proceedings of the conference on Design, Automation and Test in Europe - Volume 2**

Publisher: IEEE Computer Society

Full text available: pdf(959.72 KB) Additional Information: full citation, abstract

Customization of processor architectures through Instruction Set Extensions (ISEs) is an effective way to meet the growing performance demands of embedded applications. A high-quality ISE generation approach needs to obtain results close to those achieved by experienced designers, particularly for complex applications that exhibit regularity: expert designers are able to exploit manually such regularity in the data flow graphs to generate high-quality ISEs. In this paper, we present ISEGEN, an a ...

8 An efficient method for computing exact path delay fault coverage

B. Kapoor

March 1995 **Proceedings of the 1995 European conference on Design and Test**

Publisher: IEEE Computer Society

Full text available: pdf(536.36 KB) Publisher Site Additional Information: full citation, abstract, citings

We describe algorithms and data structures for accurate and efficient computation of path delay fault coverage. Our method uses an interval-based representation of consecutively numbered path delay faults. We describe a modified 2-3 tree data structure to store and manipulate these intervals to keep track of tested faults. Some results obtained using non-robust simulation of benchmark circuits suggest the viability of this approach.

Keywords: algorithms, automatic test software, circuit analysis computing, data structures, delays, fault diagnosis, interval-based representation, logic testing, modified 2-3 tree data structure, path delay fault coverage, tree data structures

9 New ideas in placement: Quantum-Dot Cellular Automata (QCA) circuit partitioning: problem modeling and solutions

Dominic A. Antonelli, Danny Z. Chen, Timothy J. Dysart, Xiaobo S. Hu, Andrew B. Kahng, Peter M. Kogge, Richard C. Murphy, Michael T. Niemier

June 2004 **Proceedings of the 41st annual conference on Design automation**

Publisher: ACM Press

Full text available: pdf(322.29 KB) Additional Information: full citation, abstract, references, index terms

This paper presents the Quantum-Dot Cellular Automata (QCA) physical design problem, in the context of the VLSI physical design problem. The problem is divided into three subproblems: partitioning, placement, and routing of QCA circuits. This paper presents an ILP formulation and heuristic solution to the partitioning problem, and compares the two sets of results. Additionally, we compare a human-generated circuit to the ILP and Heuristic solutions. The results demonstrate that the heuristic is ...

Keywords: circuit partitioning, computer aided design, quantum-dot cellular automata (QCA)

10 Time weaver: a software-through-models framework for embedded real-time systems

Dionisio de Niz, Raj Rajkumar

June 2003 **ACM SIGPLAN Notices , Proceedings of the 2003 ACM SIGPLAN conference on Language, compiler, and tool for embedded systems LCTES '03**, Volume 38 Issue 7

Publisher: ACM Press

Full text available: pdf(467.76 KB) Additional Information: full citation, abstract, references, citings, index terms

Embedded real-time systems are deployed in a wide range of application domains including transportation systems, automated manufacturing, process control, defense,

aerospace, and telecommunications. These systems must satisfy not only logical functional requirements but also *para-functional* properties such as timeliness, Quality of Service (QoS) and reliability. The cross-cutting behaviors imposed by these para-functional properties and dependencies on operational characteristics (e.g. ha ...

Keywords: couplers, embedded, real-time, semantic dimension, semantic separation, software-through-models

11 Graphical animation of behavior models

Jeff Magee, Nat Pryce, Dimitra Giannakopoulou, Jeff Kramer
June 2000 **Proceedings of the 22nd international conference on Software engineering**
Publisher: ACM Press

Full text available: pdf(330.44 KB) Additional Information: full citation, abstract, references, citings, index terms

Graphical animation is a way of visualizing the behavior of design models. This visualization is of use in validating a design model against informally specified requirements and in interpreting the meaning and significance of analysis results in relation to the problem domain. In this paper we describe how behavior models specified by Labeled Transition Systems (LTS) can drive graphical animations. The semantic framework for the approach is based on Timed Automata. Animations are described ...

Keywords: behvior analysis, graphic animation, labeled transition system

12 Automatic design with dependence graphs

A. E. Casavant, D. D. Gajski, D. J. Kuck
June 1980 **Proceedings of the 17th conference on Design automation**
Publisher: ACM Press
Full text available: pdf(839.36 KB) Additional Information: full citation, abstract, references, index terms

A design automation system for the design of digital systems from a high-level algorithmic description is proposed. The definition of the data-dependence graph and techniques for performing transformations that lead to optimization of hardware are described. The system can be used on several levels of design with the VLSI layout level given particular emphasis.

13 Partitioning and placement for buildable QCA circuits

Sung Kyu Lim, Ramprasad Ravichandran, Mike Niemier
March 2005 **ACM Journal on Emerging Technologies in Computing Systems (JETC)**, Volume 1 Issue 1
Publisher: ACM Press
Full text available: pdf(360.59 KB) Additional Information: full citation, abstract, references, index terms

Quantum-dot Cellular Automata (QCA) is a novel computing mechanism that can represent binary information based on spatial distribution of an electron charge configuration in chemical molecules. In this article, we present the first partitioning and placement algorithm for automatic QCA layout. We identify several objectives and constraints that will enhance the buildability of QCA circuits. The results are intended to: (1) define what is computationally interesting and could actually be built wi ...

Keywords: Nanotechnology, partitioning, placement, quantum-dot cellular automata

14 A Fast Optimal Robust Path Delay Fault Testable Adder

Bernd Becker, Rolf Drechsler, Rolf Krieger, Sudhakar M. Reddy
March 1996 **Proceedings of the 1996 European conference on Design and Test**
Publisher: IEEE Computer Society
Full text available: pdf(749.08 KB) Publisher Site Additional Information: full citation, abstract

In this paper we explore the test complexity of the adder function with respect to the robust path delay fault model. A lower bound of Omega(n2) for the cardinality of a complete test set for a combinational n-bit adder is proven. This result is valid for any adder design known until now. In addition we present a fast O(sqrt(n))-time adder that is fully robust path delay fault testable with a test set of size Theta(n^2).

Keywords: test, adder, path delay fault model

15 Diagnostic simulation of stuck-at faults in sequential circuits using compact lists

Ismed Hartanto, Srikanth Venkataraman, W. Kent Fuchs, Elizabeth M. Rudnick, Janak H. Patel, Sreejit Chakravarty
October 2001 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 6 Issue 4
Publisher: ACM Press
Full text available: pdf(185.30 KB) Additional Information: full citation, abstract, references, index terms

This article describes a diagnostic fault simulator for stuck-at faults in sequential circuits that is both time and space efficient. The simulator represents indistinguishable classes of faults as memory efficient lists. The use of lists reduces the number of output response comparisons between faults and hence speeds up the simulation process. The lists also make it easy to drop faults when they are fully distinguished from other faults. Experimental results on the ISCAS89 circuits show that t ...

Keywords: Fault simulation, stuck-at fault diagnosis

16 Survey of software tools for evaluating reliability, availability, and serviceability

Allen M. Johnson, Miroslaw Malek
September 1988 **ACM Computing Surveys (CSUR)**, Volume 20 Issue 4
Publisher: ACM Press
Full text available: pdf(3.79 MB) Additional Information: full citation, abstract, references, citings, index terms

In computer design, it is essential to know the effectiveness of different design options in improving performance and dependability. Various software tools have been created to evaluate these parameters, applying both analytic and simulation techniques, and this paper reviews those related primarily to reliability, availability, and serviceability. The purpose, type of models used, type of systems modeled, inputs, and outputs are given for each package. Examples of some of the key modeling ...

17 Using self-diagnosis to adapt organizational structures

Bryan Horling, Brett Benyo, Victor Lesser
May 2001 **Proceedings of the fifth international conference on Autonomous agents**
Publisher: ACM Press
Full text available: pdf(109.37 KB) Additional Information: full citation, abstract, references, citings, index terms

The specific organization used by a multi-agent system is crucial for its effectiveness and efficiency. In dynamic environments, or when the objectives of the system shift, the organization must therefore be able to change as well. In this paper we propose using a

general diagnosis engine to drive this process of adaptation, using the \tems\ modeling language as the primary representation of organizational information. Results from experiments employing such a system in the Producer-Cons ...

Keywords: organization and social structure, organization self-design

18 Resolving non-uniqueness in design feature histories

Vincent A. Cicirello, William C. Regli

June 1999 **Proceedings of the fifth ACM symposium on Solid modeling and applications**

Publisher: ACM Press

Full text available: pdf(1.22 MB) Additional Information: full citation, references, citings, index terms

Keywords: computer-aided design, constraint-based and parametric modeling, engineering knowledge-bases, feature-based modeling, modeling families of geometric objects, product data management

19 Timing uncertainty analysis for time-of-flight systems

John R. Feehrer, Harry F. Jordan

November 1994 **Proceedings of the 1994 IEEE/ACM international conference on Computer-aided design**

Publisher: IEEE Computer Society Press

Full text available: pdf(890.04 KB) Additional Information: full citation, abstract, references, index terms

Time-of-flight synchronization is a new digital design methodology that eliminates all latching devices, allowing higher clock rates than alternative timing schemes. Synchronization is accomplished by precisely balancing connection delays. Many effective pipeline stages are created by pipelining combinational logic, similar in concept to wave pipelining but differing in several respects. Due to the unique flow-through nature of circuits and to the need for pulse-mode operation, time-of-flig ...

20 Rapid diagnostic fault simulation of stuck-at faults in sequential circuits using compact lists

Srikanth Venkataraman, Ismed Hartanto, W. Kent Fuchs, Elizabeth M. Rudnick, Sreejit Chakravarty, Janak H. Patel

January 1995 **Proceedings of the 32nd ACM/IEEE conference on Design automation**

Publisher: ACM Press

Full text available: pdf(248.43 KB) Additional Information: full citation, references, citings, index terms

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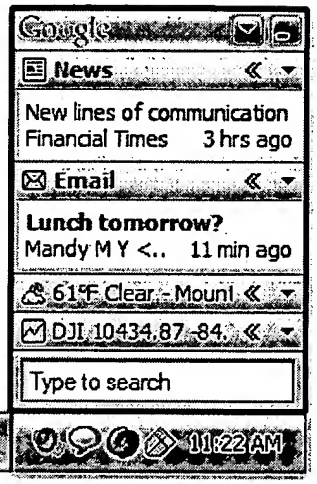
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1. **Winding technique for HT-7U Tokamak magnet coils**
Yu, J.; Gao, D.M.; Pan, Y.N.; Wu, S.T.; Wen, J.; Zhu, W.H.; Tao, Y.M.; Chen, L.P.;
Applied Superconductivity, IEEE Transactions on
Volume 10, Issue 1, March 2000 Page(s):632 - 635
Digital Object Identifier 10.1109/77.828313
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2. **The R&D progress of the EAST (HT-7U) superconducting tokamak**
Wu, S.T.; Wu, W.Y.; Pan, Y.N.; Yao, D.M.; Liao, Z.Y.; Yu, J.; Li, B.Z.; Chen, Z.M.; Pan, W.J.; Song, Wei, J.; Weng, P.O.; Gao, D.M.; Wan, Y.X.; Li, J.G.;
Fusion Engineering, 2003. 20th IEEE/NPSS Symposium on
14-17 Oct. 2003 Page(s):646 - 651
Digital Object Identifier 10.1109/FUSION.2003.1426729
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3. **Rebuilding of the HT-7 TF coil current leads**
Jie, Y.; Chen, X.Q.; Gao, D.M.; Wu, J.F.;
Fusion Engineering, 1997. 17th IEEE/NPSS Symposium
Volume 2, 6-10 Oct. 1997 Page(s):649 - 652 vol.2
Digital Object Identifier 10.1109/FUSION.1997.687710
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4. **The general assembly of HT-7 superconducting tokamak**
Gao, D.M.; Li, J.M.; Wang, S.R.; Liu, X.; Zhu, W.H.; Xue, Y.H.; Wu, J.F.; Xu, X.F.; Liu, Z.G.; Wu, H.
Fusion Engineering, 1995. SOFE '95. 'Seeking a New Energy Era'., 16th IEEE/NPSS Symposium
Volume 2, 30 Sept.-5 Oct. 1995 Page(s):1054 - 1057 vol.2
Digital Object Identifier 10.1109/FUSION.1995.534407
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5. **The fabrication and preassembly of HT-7 Superconducting Tokamak**
Gao, D.M.; Li, J.M.; Wang, S.R.; Liu, X.; Zhu, W.H.; Xue, Y.H.; Wu, J.F.; Xu, X.F.;
Fusion Engineering, 1993., 15th IEEE/NPSS Symposium on
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1 Advanced data processing in KRISYS: modeling concepts, implementation techniques, and client/server issues
Stefan Deßloch, Theo Härder, Nelson Mattos, Bernhard Mitschang, Joachim Thomas
May 1998 **The VLDB Journal — The International Journal on Very Large Data Bases**, Volume 7 Issue 2
Publisher: Springer-Verlag New York, Inc.
Full text available: pdf(210.27 KB) Additional Information: full citation, abstract, citings, index terms

The increasing power of modern computers is steadily opening up new application domains for advanced data processing such as engineering and knowledge-based applications. To meet their requirements, concepts for advanced data management have been investigated during the last decade, especially in the field of object orientation. Over the last couple of years, the database group at the University of Kaiserslautern has been developing such an advanced database system, the KRISYS prototype. In this ...

Keywords: Client/server architectures, Consistency control, Object-oriented modeling concepts, Query processing, Run-time optimization

2 Web-based remote manipulation in advanced manufacturing system
Dan Zhang, Lihui Wang
March 2005 **Proceedings of the IEEE EEE05 international workshop on Business services networks BSN '05**
Publisher: IEEE Press
Full text available: pdf(350.07 KB) Additional Information: full citation, abstract, references

In this paper, a Web-based collaborative framework is proposed. With appropriate open architecture for effective collaboration among a dispersed engineering team, the Web-based digital Shop Floor can serve real-time data from bottom up and can function as a constituent component of e-manufacturing for remote monitoring and control. This paper presents the basis of the framework for building web-based collaborative systems that can be used for distributed manufacturing. A proof-of-concept prototy ...

3 Computer-supported cooperative work in design: A Java 3d-enabled cyber workspace
Lihui Wang, Brian Wong, Weiming Shen, Sherman Lang
November 2002 **Communications of the ACM**, Volume 45 Issue 11
Publisher: ACM Press

Full text available: pdf(235.74 KB) html(24.44 KB) Additional Information: full citation, abstract, references, citings, index terms

Along with the browser paradigm, Java has fundamentally changed the work environment, helping produce compelling applications for collaborating over the Internet.

4 Shape-based retrieval and analysis of 3D models

Thomas Funkhouser, Michael Kazhdan
August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes GRAPH '04**
Publisher: ACM Press
Full text available: pdf(12.56 MB) Additional Information: full citation, abstract

Large repositories of 3D data are rapidly becoming available in several fields, including mechanical CAD, molecular biology, and computer graphics. As the number of 3D models grows, there is an increasing need for computer algorithms to help people find the interesting ones and discover relationships between them. Unfortunately, traditional text-based search techniques are not always effective for 3D models, especially when queries are geometric in nature (e.g., find me objects that fit into thi ...

5 Understanding the limitations of causally and totally ordered communication

David R. Cheriton, Dale Skeen
December 1993 **ACM SIGOPS Operating Systems Review , Proceedings of the fourteenth ACM symposium on Operating systems principles SOSP '93**, Volume 27 Issue 5
Publisher: ACM Press
Full text available: pdf(1.71 MB) Additional Information: full citation, abstract, references, citings, index terms

Causally and totally ordered communication support (CATOCS) has been proposed as important to provide as part of the basic building blocks for constructing reliable distributed systems. In this paper, we identify four major limitations to CATOCS, investigate the applicability of CATOCS to several classes of distributed applications in light of these limitations, and the potential impact of these facilities on communication scalability and robustness. From this investigation, we find limited meri ...

6 Abstracting dependencies between software configuration items

Carl A. Gunter
January 2000 **ACM Transactions on Software Engineering and Methodology (TOSEM)**, Volume 9 Issue 1
Publisher: ACM Press
Full text available: pdf(271.64 KB) Additional Information: full citation, abstract, references, index terms

This article studies an abstract model of dependencies between software configuration items based on a theory of concurrent computation over a class of Petri nets called production nets. A general theory of build optimizations and their correctness is developed based on a form of abstract interpretation called a build abstraction; these are created during a build and are used to optimize subsequent builds. Various examples of such optimizations are discusse ...

Keywords: Petri nets, abstract interpretation, mathematical models of build dependencies, software configuration management

7 Going wireless, enabling an adaptive and extensible environment

Theo G. Kanter
February 2003 **Mobile Networks and Applications**, Volume 8 Issue 1

Publisher: Kluwer Academic Publishers
Full text available: pdf(483.21 KB) Additional Information: full citation, abstract, references, index terms

This paper discusses limitations in existing and projected solutions for delivering applications to mobile users (e.g., in 3G) in an increasingly diverse heterogeneous wireless infrastructure in combination with the on-going deregulation of mobile communication and with an increasing number of more narrowly defined roles of parties participating in the delivery of applications to mobile users. Furthermore, for future service growth, users need to be the center of communication via applications t ...

Keywords: agents, context, scalability, service, wireless

8 X-GTRBAC: an XML-based policy specification framework and architecture for enterprise-wide access control

Rafae Bhatti, Arif Ghafoor, Elisa Bertino, James B. D. Joshi
May 2005 **ACM Transactions on Information and System Security (TISSEC)**, Volume 8 Issue 2
Publisher: ACM Press
Full text available: pdf(1.60 MB) Additional Information: full citation, abstract, references, index terms

Modern day enterprises exhibit a growing trend toward adoption of enterprise computing services for efficient resource utilization, scalability, and flexibility. These environments are characterized by heterogeneous, distributed computing systems exchanging enormous volumes of time-critical data with varying levels of access control in a dynamic business environment. The enterprises are thus faced with significant challenges as they endeavor to achieve their primary goals, and simultaneously ens ...

Keywords: XML, role-based access control, secure enterprises

9 Data organization and retrieval on parallel air channels: performance and energy issues

J. Juran, A. R. Hurson, N. Vijaykrishnan, Soontae Kim
March 2004 **Wireless Networks**, Volume 10 Issue 2
Publisher: Kluwer Academic Publishers
Full text available: pdf(238.18 KB) Additional Information: full citation, abstract, references, index terms, review

Our interest in the global information sharing process is motivated by the advances in communication and computation technologies, the marriage between the two technologies, and the almost limitless amount of information available on the network. Within the scope of the global information sharing process, when a user's request (potentially mobile) is directed to public data, broadcasting has been suggested as an effective mechanism to access data. The effectiveness of the schemes to retrieve pub ...

Keywords: access latency, energy awareness, global information system, indexing, mobile devices, scheduling, wireless communication

10 Time weaver: a software-through-models framework for embedded real-time systems

Dionisio de Niz, Raj Rajkumar
June 2003 **ACM SIGPLAN Notices , Proceedings of the 2003 ACM SIGPLAN conference on Language, compiler, and tool for embedded systems LCTES '03**, Volume 38 Issue 7
Publisher: ACM Press
Full text available: pdf(467.76 KB) Additional Information: full citation, abstract, references, citings, index

terms

Embedded real-time systems are deployed in a wide range of application domains including transportation systems, automated manufacturing, process control, defense, aerospace, and telecommunications. These systems must satisfy not only logical functional requirements but also *para-functional* properties such as timeliness, Quality of Service (QoS) and reliability. The cross-cutting behaviors imposed by these para-functional properties and dependencies on operational characteristics (e.g. ha ...

Keywords: couplers, embedded, real-time, semantic dimension, semantic separation, software-through-models

11 eSCA: a thin-client/server/Web-enabled system for distributed supply chain simulation

H. Bob Chen, Oliver Bimber, Chintamani Chhatre, Elizabeth Poole, Stephen J. Buckley

December 1999 **Proceedings of the 31st conference on Winter simulation: Simulation--a bridge to the future - Volume 2**

Publisher: ACM Press

Full text available: pdf(118.87 KB) Additional Information: full citation, references, citings, index terms

12 Implementing shared manufacturing services on the World-Wide Web

J. W. Erkes, K. B. Kenny, J. W. Lewis, B. D. Sarachan, M. W. Sobolewski, R. N. Sum

February 1996 **Communications of the ACM**, Volume 39 Issue 2

Publisher: ACM Press

Full text available: pdf(404.10 KB) Additional Information: full citation, references, citings, index terms

13 Efficient filtering in publish-subscribe systems using binary decision diagrams

Alexis Campailla, Sagar Chaki, Edmund Clarke, Somesh Jha, Helmut Veith

July 2001 **Proceedings of the 23rd International Conference on Software Engineering**

Publisher: IEEE Computer Society

Full text available: pdf(225.86 KB) Publisher Site Additional Information: full citation, abstract, references, citings, index terms

Implicit invocation or publish-subscribe has become an important architectural style for large-scale system design and evolution. The publish-subscribe style facilitates developing large-scale systems by composing separately developed components because the style permits loose coupling between various components. One of the major bottlenecks in using publish-subscribe systems for very large scale systems is the efficiency of filtering incoming messages, i.e., matching of published events w ...

14 Provisioning: Design, implementation, and evaluation of a client characterization driven web server

Balachander Krishnamurthy, Yin Zhang, Craig E. Wills, Kashi Vishwanath

May 2003 **Proceedings of the 12th international conference on World Wide Web**

Publisher: ACM Press

Full text available: pdf(184.71 KB) Additional Information: full citation, abstract, references, citings, index terms

In earlier work we proposed a way for a Web server to detect connectivity information about clients accessing it in order to take tailored actions for a client request. This paper describes the design, implementation, and evaluation of such a working system. A Web

site has a strong incentive to reduce the 'time-to-glass' to retain users who may otherwise lose interest and leave the site. We have performed a measurement study from multiple client sites around the world with various levels of conn ...

Keywords: apache server, client classification, content delivery, httperf, server adaptation, web performance

15 Use of simulation to test client-server models

Yogesh L. Deshpande, Roger Jenkins, Simon Taylor
November 1996 **Proceedings of the 28th conference on Winter simulation**
Publisher: ACM Press
Full text available: pdf(697.52 KB) Additional Information: full citation, references, citings

16 Sharing manufacturing information in virtual enterprises

Martin Hardwick, David L. Spooner, Tom Rando, K. C. Morris
February 1996 **Communications of the ACM**, Volume 39 Issue 2
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17 Transaction-centric reconciliation in disconnected client-server databases

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As mobile computing devices become more and more common, mobile databases are becoming popular. An important feature of these database systems is their ability to allow optimistic replication of data by providing disconnected mobile devices the ability to perform local updates. The key problem to this approach is the reconciliation problem, i.e. the problem of serializing potentially conflicting updates from disconnected clients on all replicas of the database. Reconciliation of conflicting u ...

Keywords: databases, disconnected operation, reconcilation, serializability testing

18 Pushing dependent data in clients-providers-servers systems

Amotz Bar-Noy, Joseph Naor, Baruch Schieber
September 2003 **Wireless Networks**, Volume 9 Issue 5
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In satellite and wireless networks and in advanced traffic information systems in which the up-link bandwidth is very limited, a server broadcasts data files in a round-robin manner. The data files are provided by different providers and are accessed by many clients. The providers are independent and therefore files may share information. The clients who access these files may have different patterns of access. Some clients may wish to access more than one file at a time in any order, some clien ...

Keywords: broadcast disks, clients-providers-servers, de-randomization, scheduling

19 Software architecture: Business-structured client/server: an architecture for distributed applications

Dominic R. Cols

October 1993 **Proceedings of the 1993 conference of the Centre for Advanced Studies on Collaborative research: software engineering - Volume 1**

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The drive towards 'downsizing' from centralized, mainframe systems to distributed systems is based on the expectation of lower costs and greater flexibility from using cheaper MIPS and workstations. The result, however, is a much more complex network of distributed systems, running with a variety of operating systems, database managers, transaction processors, and applications.The author will show that, by using current application design techniques, the cost of deploying and maintaining applica ...

20 Growing pains—and successes—in transforming the information systems organization for client/server development

William D. Nance

January 1995 **ACM SIGCPR Computer Personnel**, Volume 16 Issue 1

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Full text available: pdf(908.20 KB) Additional Information: full citation, abstract, citings, index terms

Information systems (IS) groups are under increasing pressure to contribute to organizational performance and to support, or even drive, broad organizational transformation efforts through the successful exploitation of information technology (IT). Using a "sociocentric" model of organizational work, this paper analyzes the experiences of one company's IS group that recently embarked on a long-term, enterprise-wide client/server system development initiative designed to transform organizational ...

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